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Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Shahriar Ahmed, et al.

Application No. 10/633,055

Filed: 07/31/2003

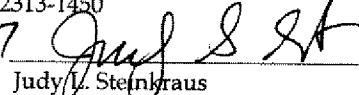
For: SUPER SELF-ALIGNED
COLLECTOR DEVICE FOR MONO-
AND HETERO BIPOLAR JUNCTION
TRANSISTORS, AND METHOD OF
MAKING SAME

Examiner: Im, Junghwa M.

Art Unit: 2811

FIRST CLASS CERTIFICATE OF MAILING

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on 6/26/07 
Date Judy L. Steintraus

Commissioner For Patents
Alexandria VA 22313-1450

TERMINAL DISCLAIMER UNDER 37 C.F.R. § 1.321(c)

The undersigned attorney represents that the undersigned attorney is the attorney of record for the above-referenced patent application.

The assignee of the entire right, title, and interest in and to the above-referenced patent application is Intel Corporation ("assignee"), a Delaware corporation having a place of business at 2200 Mission College Boulevard, Santa Clara CA 95052-8119.

The terminal part of any patent granted on the above-identified application that would extend beyond the expiration of the full statutory term of

X United States Patent No. 6,703,685, entitled SUPER SELF-ALIGNED COLLECTOR DEVICE FOR MONO-AND HETERO BIPOLAR JUNCTION TRANSISTORS, AND METHOD OF MAKING SAME, and issued March 9, 2004, as presently shortened by any terminal disclaimer,

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is hereby disclaimed, except as provided below, and it is agreed that any patent so granted on the above-identified application shall be enforceable only for and during such period that the legal title to said patent shall be the same as the legal title to

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No disclaimer is being made as to any terminal part of any patent granted on the above-identified application prior to the expiration of the full statutory term of

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statutory term as presently shortened by any terminal disclaimer, except for the separation of legal title stated above.

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Please charge Deposit Account No. 02-2666 for any fee deficiency that may be due. A duplicate of this Terminal Disclaimer is enclosed for Deposit Account charging purposes.

Respectfully submitted,

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